

PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

of 2

Complete if Known

Application Number	09/833,438
Filing Date	11 April 2001
First Named Inventor	Seven, et al.
Art Unit	2682
Examiner Name	Nguyen, Lee
Attorney Docket Number	2238-080

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 5,621,355	04/15/1997	Williams, et al.	
		US- 5,937,013	08/10/1999	Lam, et al.	
		US- 4,193,686	03/18/1980	Klank	
		US- 5,281,931	01/25/1994	Bailey, et al.	
		US- 5,564,099	10/08/1996	Yonekura, et al.	
		US- 5,404,589	04/04/1995	Bijker, et al.	
		US- 5,758,296	05/26/1998	Nakamura	
		US- 3,894,286	07/08/1975	Armstrong	
		US- 6,167,246	12/26/2000	Elder, et al.	
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FOREIGN PATENT DOCUMENTS

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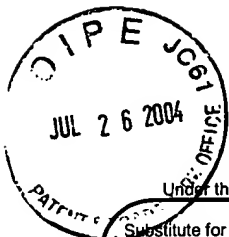
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		AJIKUTIRA, et al "A Fully-Integrated CMOS RFIC for Bluetooth Applications" 2001 IEEE International Solid-State Circuits Conference, Pgs 198-199, 2001.	
		FILIOI, et al; "A 22mW Bluetooth RF Transceiver with Direct RF Modulation and On-Chip IF Filtering" 2001 IEEE International Solid-State Circuits Conference, Pgs. 202-203, 2001.	
		JOHNS & MARTIN "Analog Intergrated Circuit Design" Chapter 15, Pgs. 574-575, 578-584 & 626-635, Published by John Wiley & Sons, Inc., 1996.	

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